



||Jai Sri Gurudev ||

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RESUME

Name	HARISH T L			
Date of Birth	11/4/2000			
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Educational Qualifications	Exam Passed (Pl. Tick)	Institution University	% & Class Obtained	Year
	Degree: B.E	S.S.I.T, TUMAKUR	6.36	2021
	PG: M.Tech	S.S.T.T, TUMAKUR	8.65	2024
	Higher:Ph.D			
	Others			
Experience	Nature of Experience		No. of Years	
	Teaching		2 Yrs	
	Industry/Research		1.2 YRS	
	Total No. of years of Experience		1.2 Yrs	
Experience Details	Designation	Institution/ Organization	Duration	
	Guest Lecture	GOVT. POLYTECHNIC, TUMAKUR	2011-2023	
	Assistant Professor	SJC INSTITUTE OF TECHNOLOGY	01/AUG/2025	
Other Professional Experience	NA			

Areas of Research Interest & Guidance	VLSI Design
Distinctions/Awards Received	M.TECH IN FIRST CLASS WITH DISTINCTION
National/ International Work Shops/ Seminars / Conferences Attended	National Conference on AI & ML in Aerospace & Biomedical Engineering Applications (NCABEA-2023) International Journal of Science & Engineering Development Research (Design of AXI4 Slave Device Using Verilog Volume 8 Issue 6, June-2023) International Journal of Science & Engineering Development Research (Verification of an AXI4 Slave Device Using System Verilog Volume 8 Issue 6, June-2023) Second International Conference on Computing, Communication and Cyber-Physical Systems (IC3CPS 2023)
No. of Papers Presented/ Books Published	0
Additional Information (patents if any)	-----

Date:12-11-2025

Signature of the Candidate
(HARISH T L)