



||Jai Sri Gurudev ||

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RESUME

Name	HARISH T L			
Date of Birth	11/04/2000			
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Educational Qualifications	Exam Passed (Pl. Tick)	Institution University	% & Class Obtained	Year
	Degree: B.E	Sri Siddhartha Institute of Technology	6.36	2021
	PG: M.Tech	Sri Siddhartha Institute of Technology	8.65	2024
	Higher:Ph.D	NA		
	Others			
Experience	Nature of Experience		No. of Years	
	Teaching		0	
	Industry/Research		1.2	
	Total No. of years of Experience		1.2	
Experience Details	Designation	Institution/ Organization	Duration	
	PD ENGINEER	Radiant semiconductors pvt Ltd	2023-2025	
Other Professional Experience	NA			

Areas of Research Interest & Guidance	NA
Distinctions/Awards Received	First class with Distinction in M.tech
National/ International Work Shops/ Seminars / Conferences Attended	2 National conference, 2 International conference
No. of Papers Presented/ Books Published	4 paper published
	Review on design and verification of an advanced extensible interface slave device (National conference)
	Design of AXI4 SLAVE DEVICE USING VERILOG
	Verification of an AXI4 slave device using System Verilog
	System Verilog verification techniques for AXI4 slave device: A practical approach
Additional Information (patents if any)	NA

Harish T L

Date: 01/08/2025

Signature of the Candidate