

AICTE-IDEA (Idea Development, **Evaluation & Application) LAB SCHEME**













Date: 09.01.2023

To,
The Principal,
SJC Institute of Technology,
Chickballapur-562101

Through,
Dr. B N Shobha,
HOD, Electronics & Communication Engineering,
SJC Institute of Technology,
Chickballapur-562101

Respected Sir,

Sub: Requesting permission to conduct 3 Days Hands-on workshop on PCB Designing for the students.

With reference to the above subject, we are planning to conduct "3 Days Hands on Workshop on PCB Designing", for V Sem students under AICTE Activity Idea Lab scheme, on 10th -12th January 2023. The program schedule, brochure and the contents to be covered are attached here within.

Jemit P

Kindly grant the permission for conducting the same.

Thanking you Sir,

Yours faithfully,

(Dr. B.N. Shohha)

(Dr. B N Shobha)
Prof. & Head
Dept of Electronics & Communication Engg
S.J.C. Institute of Technology
CHICKBALLAPUR - 562 101

|| Jai Sri Gurudev ||



SIC INSTITUTE OF TECHNOLOGY

Chickballapur- 562101, Karnataka, India (VTU Affiliated, AICTE Approved, Accredited by NAAC & NBA)







AICTE IDEA LAB

is organising

3-days Hands on Workshop on PCB Designing



DATE: 10-12TH JAN 2023

Venue: IDEA Lab, SJCIT



TECH GURUS

Dr. Pranjala Tiwari Dr. Krushitha Shetty Prof. E. N. Srivani

Faculty Coordinator

Dr. Bhaskar S. ECE, SJCIT Chickballapur

Faculty Co-coordinator

Dr. B. N. Shobha HOD, ECE, SJCIT Chickballapur

Organising chair

Mr. Suresh J.Registrar, SJCIT
Chickballapur

Chief Mentor

Dr. G. T. RajuPRINCIPAL, SJCIT
Chickballapur



||Jai Sri Gurudev||

S J C Institute of Technology

P B No.20, BB Road, Chicklapur-562101 Department of Mechanical Engineering



AICTE IDEA LAB 3-days Hands on Workshop on PCB Designing

10-12th January 2023 Venue: IDEA Lab, SJCIT

Program Schedule

Day/Date	Time	Name and Organization of the Resource Person	Торіс
Day-1 Tuesday [10/01/2023]	9:30-10:30 am 10:30-11:30 pm	Dr. Pranjala Tiwari Associate Professor, ECE, SJCIT	Introduction to Advanced PCB designing technology. Familiarization with Ki-cad software for building and designing the circuit
	11:30-12:00 pm	Dr. Krushitha Shetty Assistant Professor, ECE, SJCIT	Steps involved in manual PCB designing process
	1:00 -2:00 pm 2:00-3:30 pm		Circuit implementation on breadboard Circuit designing on Ki-cad
<i>Day-2</i> Wednesday [11/01/2023]	1:00- 4:00 pm	Dr. Pranjala Tiwari Dr. Krushitha Shetty Prof. E.N.Srivani	Assigning topics and components to each group with the circuit diagram Implementation on Breadboard
<i>Day-3</i> Thursday [12/01/2023]	9:30-4:00 pm		Designing on Ki-cad software Hands on PCB designing individually for each group

Contents to be covered:

- 1. History and evolution of PCB circuits
- 2. THT vs SMD PCB technology
- 3. Familiarization with Ki-cad software interface
- 4. Designing of basic circuits using the software library
- 5. Fabrication of PCB boards using CNC router
- 6. Hands-on manual fabrication process
- 7. Preparation of custom designed PCB Boards

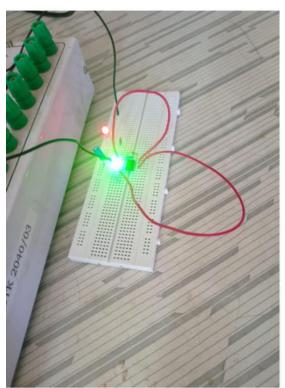
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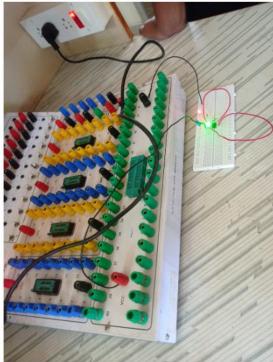
Meher ujwala N R 1SJ20EC090	Saurabh (1SJ20EC133)	Namitha K S 1SJ20EC094
Madhushree N 1SJ20EC080	Meghana N	Megha shree Y V 1SJ20EC089
Navitha_K 1SJ20EC098 Prathima S 1SJ20EC112		Neha V 1SJ20EC100
Meher uijwala - 1SJ20EC090	R Saikrithi - 1SJ20EC116	Namitha k s -1SJ20EC094
MANOJ.M 1SJ20EC083	KEERTHI KUMAR.A 1SJ20EC067	LAKSHMI SAGAR.D 1SJ20EC075
LOKESH G V (1sj20ec078)	Ramesh g v(1SJ20EC121)	Naveen K N(1SJ20EC097)
Shreyas,S.R. [1SJ20EC136]	Sudarshan G [1SJ20EC147]	Varsha siri,M [1SJ20EC172]
Shahameer Taj 1SJ20EC134	Sahana D 1SJ20EC131	Roushini 1SJ20EC126
Kayana H (1SJ20EC064)	Keerthana S (1SJ20EC066)	Meghana K (1SJ20EC072)
Rajini H <u>S_(</u> 1SJ20EC118)	Rakshitha B A (1SJ20EC119)	Meghana M (1SJ20EC086)
Shahameer Taj (1SJ20EC134)	Sahana D (1SJ20EC131)	Roushni (1SJ20EC126)
Shahameer taj (1SJ20EC134)	Sahana D(1SJ20EC131)	Roushni (1SJ20EC126)
Thimmateddy K B 1SJ20EC159	Ravikiran H K 1SJ20EC123	Bharath Gowda A S 1SJ21EC401
Bhavana H N (1SJ20EC022)	Deeksha R (1SJ20EC038)	Adlin Ananya V (1SJ20EC002)
Lokesh G V 1sj20ec078	Naveen K N 1sj20ec097	Pavan Kumar T J 1sj20ec104
1SJ20EC058	1SJ20EC044	1SJ20EC043
Srilakshmi A=1SJ20EC144	Thanu shree S=1SJ20EC156	Varshini S=1SJ20EC174
SAMYAMA C 1SJ20EC132	SHUBHA D_1SJ20EC137	RUKSAR R 1SJ20EC127
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Annavajjala Niketh Sandilya_(1SJ20EC010)	Gottipati Prem Kumar (1SJ20EC050)	Sudeep Reddy (1SJ20EC051)
Tejas Gowda. R	Tharun V	Sri kiran
	I	I
Sonika N(1SJ20EC143)	Poornima L (1SJ20EC105)	Likhitha N.M (1SJ20EC076)
DILIP BE (1SJ20EC041)	AKASH V (1SJ20EC004)	AJAY KUMAR R (1SJ20EC003)
Bhanushree K.M	Chandan S	Anusha KM
1SJ20EC036	1SJ20EC031	1SJ20EC046
Chandan Kumar A V (1SJ20EC028)	Abhishek H (1SJ20EC001)	Ankith gowda M (1SJ20EC009)
Bhargavi S (1SJ20EC021)	Chinmaye C G(1SJ20EC033)	K Bhumipriya (1SJ20EC059)
Punith H 1SJ20EC115	Puneeth M 1SJ20EC114	Prashant D R 1SJ20EC109
P S Vishwanath Koushik _1SJ20EC103	Pranith S V _1SJ20EC108	Manoj N C_1SJ20EC084
VYSHNAVI L (1SJ20EC179)	TARIGOPULA DINESH KARTHIK CHOWDHARY (18J20EC150)	VARSHA MAHESHWAR (1SJ20EC171)
Chandu shree T A (1SJ20EC030)	Bhargavi A S (1SJ20EC020)	Divya Reddy P R (1SJ20EC042)
Rakshitha k	Hari Prasad N	Anxitha L
T Jithendra (1SJ20EC158)	Shreyas Kumar A (1SJ20EC135)	Nizamuddin V S (1SJ20EC176)
Harshitha A-1SJ20EC056	Chirayu N-1SJ20EC035	Anusha G M-1SJ20EC012
Chinmage C G - 1SJ20EC033	Bhargavi S - 1S20EC021	Gandluri Kiran Kumar - 1SJ20EC048
Hemanth Kumar C 1SJ20EC057	Kondurukoushikkumarraju1SJ20EC071	Hari Krishna G 1SJ20EC053
Gongatii geethika(1SJ20EC049)	B V DEEPIKAPOORNIMA (1SJ20EC017)	K Nandini (1SJ20EC061)
subhan c (1SJ20EC146)	s upendra (1SJ20EC130)	saurabh (1SJ20EC133)
Gongati Geethika-1SJ20EC049	B. V Deepika poornima-1SJ20EC017	Boyapati Harshitha- 1SJ20EC025
D V MADHURI(1SJ20EC37)	T VYSHNAVI(1SJ20EC149)	N DHANA SREE(1SJ20EC099)
SAMYAMA C 1SJ20EC132	VYSHNAVI R 1SJ20EC180	SMITHA N 1SJ20EC141
Vandana Gangothri V (1SJ20EC168)	1SJ20EC166	18J20EC095
Uday Kiran j ,1SJ20EC162	Tejas G S 1SJ20EC152	Vankadaru Sai gayathri 1SJ20EC170

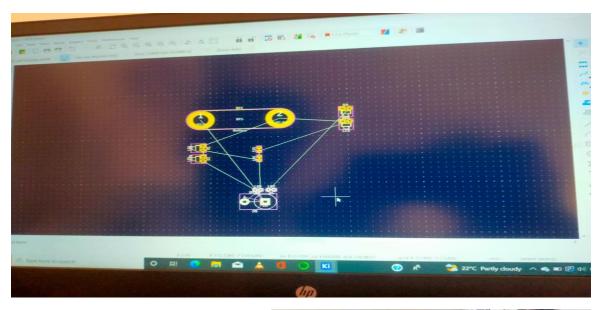
Event photos

















Quiz Questions

- 1. Which technology makes the components to be inserted on board with pre-drilled holes?
- 2. Where the components placed on the board are soldered?
- 3. How many layers does a PCB have?
- 4. Traces and planes utilized in PCB designing comprises of _____?
- 5. How a PCB board can be designed initially?
- 6. What is the further step in PCB design after the libraries get ready?
- 7. What verifies whether the circuit connections work properly?
- 8. What is the further step in PCB design after the schematic?
- 9. What is the common variety of PCBs available?
- 10. How many conducting layers are present in Single-sided PCB?

Quiz report

