||Jai Sri Gurudev ||

S J C Institute of Technology, Chikkballapur – 562 101 RESUME

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| **Name** | Asharani M |
| **Date of Birth** | 14.02.1992 |
| **Address** | ***ContactAddress:*** Vapasandra Chickballapur, Karnataka***Residential Address:*** Vapasandra Chickballapur, Karnataka ***ContactNos:*** +91-9945951116***E-mail***: asharani2258@gmail.com**Mobile:** +91-9945951116 |
| **Department / Discipline** |  |
| **Educational Qualifications** | **Exam Passed****(Pl. Tick)** | **Institution | University** | **% & Class Obtained** | **Year** |
| **Degree:**B.E | Sri Krishna Rajendra Silver Jubilee Tecnological Institute | 75.89FCD | 2016 |
| **PG:**M.Tech | Delhi Technological University | 9.29FCD | 2018 |
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| **Experience** | **Nature of Experience** | **No. of Years** |
| Teaching | - |
| Industry/Research |  0.6 |
| Total No. of years of Experience | 0.6 |
| **Experience Details** | **Designation** | **Institution/ Organization** | **Duration** |
| Assistant Professor | SJCIT, Chikkballapur | 08/08/2022To Present |
| **Awards and Achievement** | **1.** Qualified GATE examination , dated august 2016.**2**. Awarded best project for M.Tech final year, dated September 2018.**3**. Received merit recognition certificate and scholarship as college topper in 12th class, dated April 2012. |

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| **Areas of Research Interest & Guidance** | Low Power VLSI Design |
| **Distinctions/Awards Received** | 6 |
| **National/ International Work Shops/ Seminars / Conferences Attended** | 3 |
| **No. of Papers Presented/ Books Published** | 1 (Paper Presented) |
|  | 1. Asharani M, P Nagabushanam, A 2.4GHz Algorhmic design of CMOS LNA in 180nm Technology, at 3rd IEEE International conference on RTEICT-2018, ISBN- 978-1-5386-2440-1.
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**Date: 29.08.2022**

 **Signature of the Candidate**